Klok Systems
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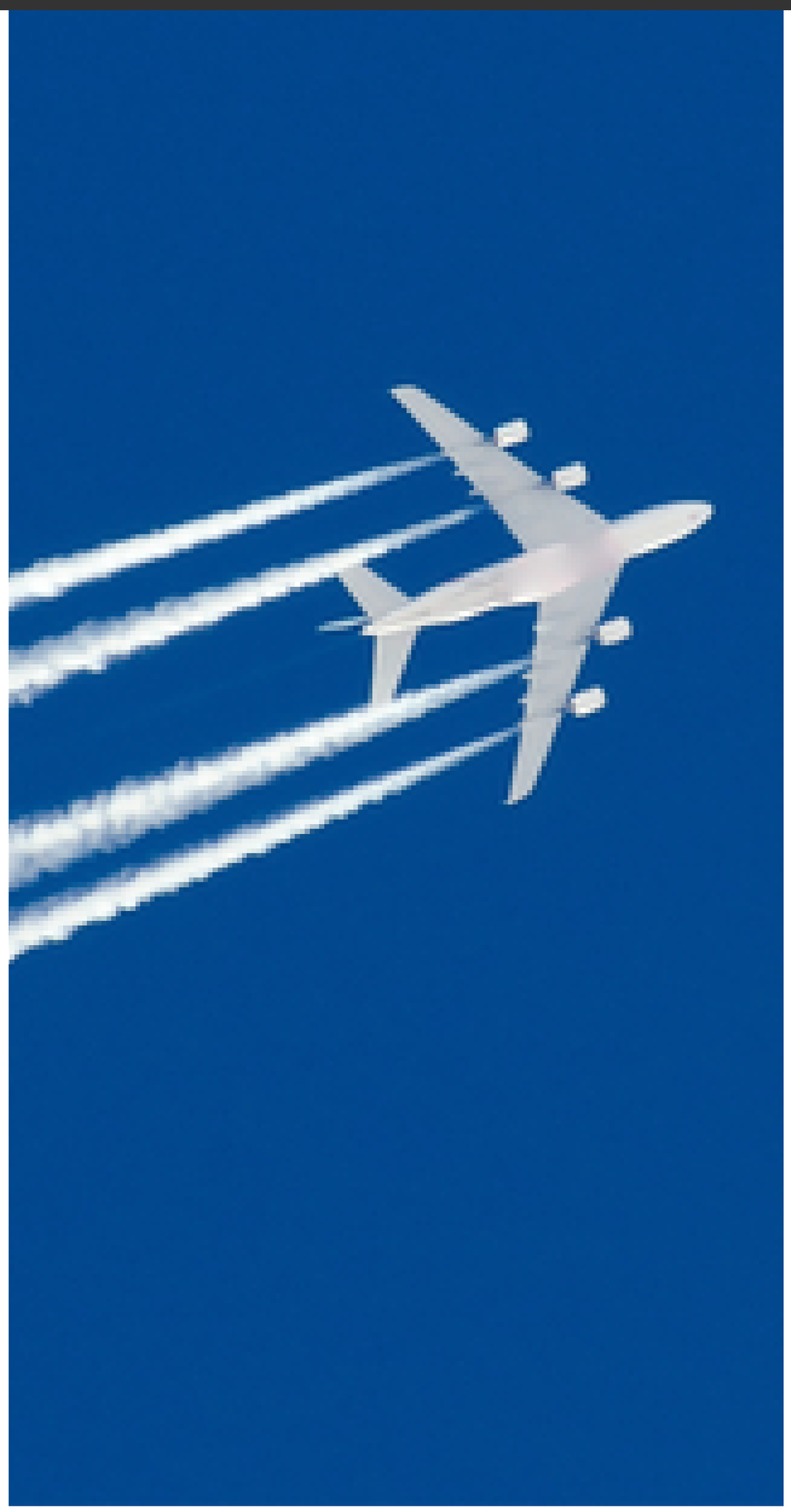


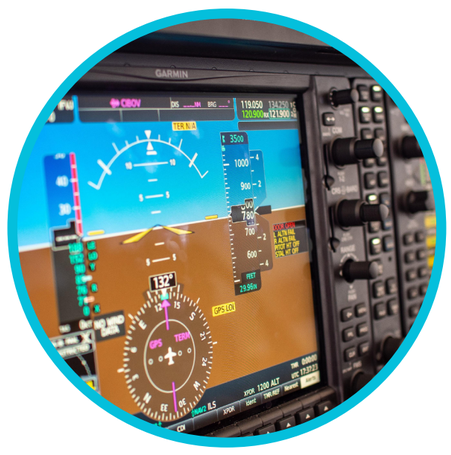
ARINC 818 IP CORE

CERTIFIABLE AVIONICS VIDEO TRANSCEIVER IP CORE

ARINC 818 IP Core is a complete certifiable solution for both transmit and receive applications. This ARINC 818 compliant soft IP Core protocol can be implemented on any FPGA.

This IP core can be used in Avionics Displays and Vision Systems and Test Beds. Video parameters can be configured using the interfaces. It can be used for streaming videos in either interlaced or progressive format. Different types of imgaes are supported, viz., RGB, RGBA and yCbCr.

Supports AXI and PCIe as host interfaces. IP Core comes with well defined ICD and Sample Code to make it easier for your integration.



This IP Core provides video transceiver interface for easy integration into your Avionics systems. Quality of resolution depends on the FPGA transceiver speed used for implementation. ARINC-818 IP can be implemented on any transceiver based Xilinx FPGA.

IP Core supports variable line speed. It's easy to integrate user logic in Xilinx FPGA. Supports flexible video resolution, various pixel packing and input format, thus making it usable across Avionics systems and requirements.

Klok Systems' ARINC 818 IP Core is developed by Avionics engineers with decades of experience. Klok Systems has in-depth experience in Avionics Engineering, including DO-178B/C, DO-254 and DO-160 G/F. We can provide necessary engineering support to your team during the certification process.

KEY FEATURES INCLUDE

- Configurable resolution
- CRC-based Safety Features
- Can be easily integrated
- Supports 1/2/3/4G data rates
- Exception support by team
- Certifiable to DO-254 DAL 'A'

Klok Systems also offers Software, Electronics and FPGA Avionics Engineering Services as per DO-178B/C, DO-254 and DO-160 F/G, DAL A through D.

Reach us out at contactus@kloksys.com for more information.